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Attorney Docket No.: 108298734US

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Date: November 1, 2005 By: Stephen P. Whelan
Stephen P. Whelan

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: LARRY D. KINSMAN

PATENT NO.: 6,934,065

ISSUED: AUGUST 23, 2005

FOR: **MICROELECTRONIC DEVICES AND
METHODS FOR PACKAGING
MICROELECTRONIC DEVICES**

EXAMINER: LOHA BEN

ART UNIT: 2873

CONF. NO.: 7053

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**Request for Certificate of Correction
under 37 C.F.R. §1.322 or §1.323**

*Certificate
NOV 08 2005
of Correction*

Sir:

1. The applicant(s) requests a Certificate of Correction to correct errors in the above-identified patent, which are listed on the enclosed Form PTO/SB/44.
2. Any errors on the part of the applicant are of a clerical or typographical nature or are otherwise minor in character. None of the requested corrections would constitute new matter or require reexamination of the patent.
3. Source of Error(s) and Payment of Fee:
 - All of the errors listed on Form PTO/SB/44 are believed to be due to mistake on the part of the USPTO (37 C.F.R. §1.322). Accordingly, no fees are believed to be due.
 - At least one of the errors occurred due to applicant's mistake made in good faith (37 C.F.R. §1.323).
 - A check covering the fee under 37 C.F.R. §1.20(a) (\$100.00) is enclosed herewith.
 - Please charge the fee under 37 C.F.R. §1.20(a) to Deposit Account No. 50-0665. This paper is provided in triplicate.

Please charge any underpayment necessary for consideration of this paper to Deposit Account No. 50-0665.

4. Please send the Certificate of Correction to the undersigned at the address shown below.

Date: 10/31/05

Respectfully submitted,
Perkins Coie LLP



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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,934,065 *12*

DATED : August 23, 2005

INVENTOR(S) : Larry D. Kinsman

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Front Page

References Cited, Please insert the following references, cited in an Information Disclosure Statement submitted March 15, 2005 and initiated by the Examiner April 20, 2005:

-6,407,381	Glenn et al.	06/18/2002
2004/0038442	Kinsman	02/26/2004
2004/0041261	Kinsman	03/04/2004
10/893,022	Hall et al.	07/16/2004
10/901,851	Derderian et al.	07/28/2004
10/915,180	Street et al.	08/10/2004
10/927,550	Derderian et al.	08/26/2004

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Micro Chem, Nano SU-8, Negative Tone Photoresist Formulations 50-100, 4 pages, February 2002, <http://www.microchem.com/products/pdf/SU8_50-100.pdf>.

BRUBAKER, C. et al., "Ultra-thick Lithography for Advanced Packaging and MEMS," SPIE's 27th Annual International Symposium on Microlithography 2002, March 3-8, 2002, Santa Clara, CA.

AUSTIN, M.D. and S.Y. CHOU, "Fabrication of 70 nm channel length polymer organic thin-film transistors using nanoimprint lithography," Applied Physics Letters, Vol. 81, No. 23, pp. 4431-4433, December 2, 2002, American Institute of Physics;--

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PATENT NO. 6,934,065

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CERTIFICATE OF CORRECTION

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